



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Rinco-Mora, et al.

Art Unit: 2816

Serial No.: 09/977,609

Examiner: Nguyen, Minh

Filing Date: 10/15/01

Docket No.: TI-33257

Title: A SIMPLE FLOATING-GATE MOS THRESHOLD VOLTAGE EXTRACTION  
CIRCUIT & AN ACCURATE LINEAR CAPACITOR-MULTIPLIER CIRCUIT

5/11/02  
Carri.  
Meresa  
10/11/02  
RECEIVED  
OCT -4 2002  
TECHNOLOGY CENTER-2800

LETTER TO THE OFFICIAL DRAFTSPERSON

Assistant Commissioner for Patents  
ATTN: Official Draftsperson  
Washington, D.C. 20231

APPROVED BY TIE  
m  
11/14/02

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)  
I hereby certify that the above correspondence is being  
deposited with the U.S. Postal Service as First Class Mail  
in an envelope addressed to: Assistant Commissioner for  
Patents, Washington, D.C. 20231 on:

9-26-02

Tommie Chambers  
Tommie Chambers

Dear Sir:

Please correct Figures 1-3 and 7 as noted in red on the enclosed drawing.

Charge any necessary fee to Deposit Account No. 20-0668. This authorization is  
enclosed in triplicate.

Respectfully submitted,

W. Daniel Swayze, Jr.  
W. Daniel Swayze, Jr.  
Reg. No. 34,478  
Attorney for Applicants

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